

Single Output Mode

PWM Mode

	nybble 1				nybble 2				nybble 3				nybble 4				
	T	O	C	C	a	1	M	O	D	D	D	D	L	L	L	L	
Channel 1 A	1	0	0	0	0	1	0	0					1	0	x	x	+ nybble 1
Channel 1 B	1	0	0	0	0	1	0	1					0	1	0	x	+ nybble 2
Channel 2 A	1	0	0	1	0	1	0	0					x	x	x	x	+ nybble3
Channel 2 B	1	0	0	1	0	1	0	1					1	1	1	1	+ OxF
Channel 3 A	1	0	1	0	0	1	0	0					X	X	X	X	=
Channel 3 B	1	0	1	0	0	1	0	1									
Channel 4 A	1	0	1	1	0	1	0	0									
Channel 4 B	1	0	1	1	0	1	0	1									
Float									0	0	0	0	X	X	X	X	
PWM Fwd 1									0	0	0	1	X	X	X	X	
PWM Fwd 2									0	0	1	0	X	X	X	X	
PWM Fwd 3									0	0	1	1	X	X	X	X	
PWM Fwd 4									0	1	0	0	X	X	X	X	
PWM Fwd 5									0	1	0	1	X	X	X	X	
PWM Fwd 6									0	1	1	0	X	X	X	X	
PWM Fwd 7									0	1	1	1	X	X	X	X	
Brake then Float									1	0	0	0	X	X	X	X	
PWM Rev 7									1	0	0	1	X	X	X	X	
PWM Rev 6									1	0	1	0	X	X	X	X	
PWM Rev 5									1	0	1	1	X	X	X	X	
PWM Rev 4									1	1	0	0	X	X	X	X	
PWM Rev 3									1	1	0	1	X	X	X	X	
PWM Rev 2									1	1	1	0	X	X	X	X	
PWM Rev 1									1	1	1	1	X	X	X	X	

LRC values are calculated by adding the columns and indicating if they are odd (1) or even(0).

C C = Channel 1 - 4 O = Output A or B