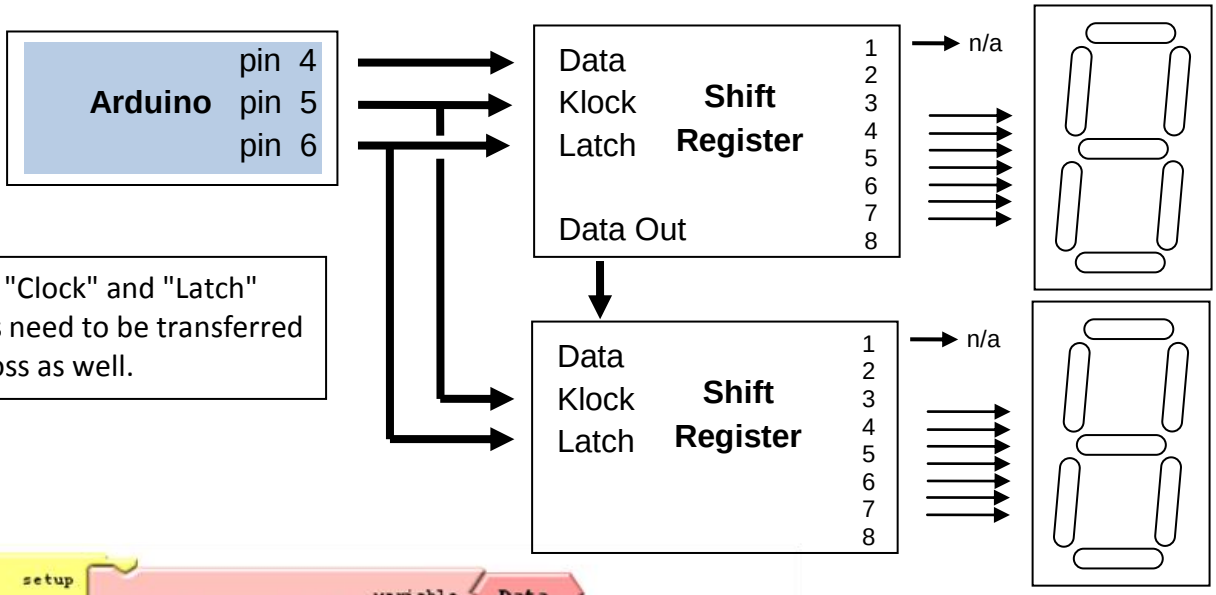
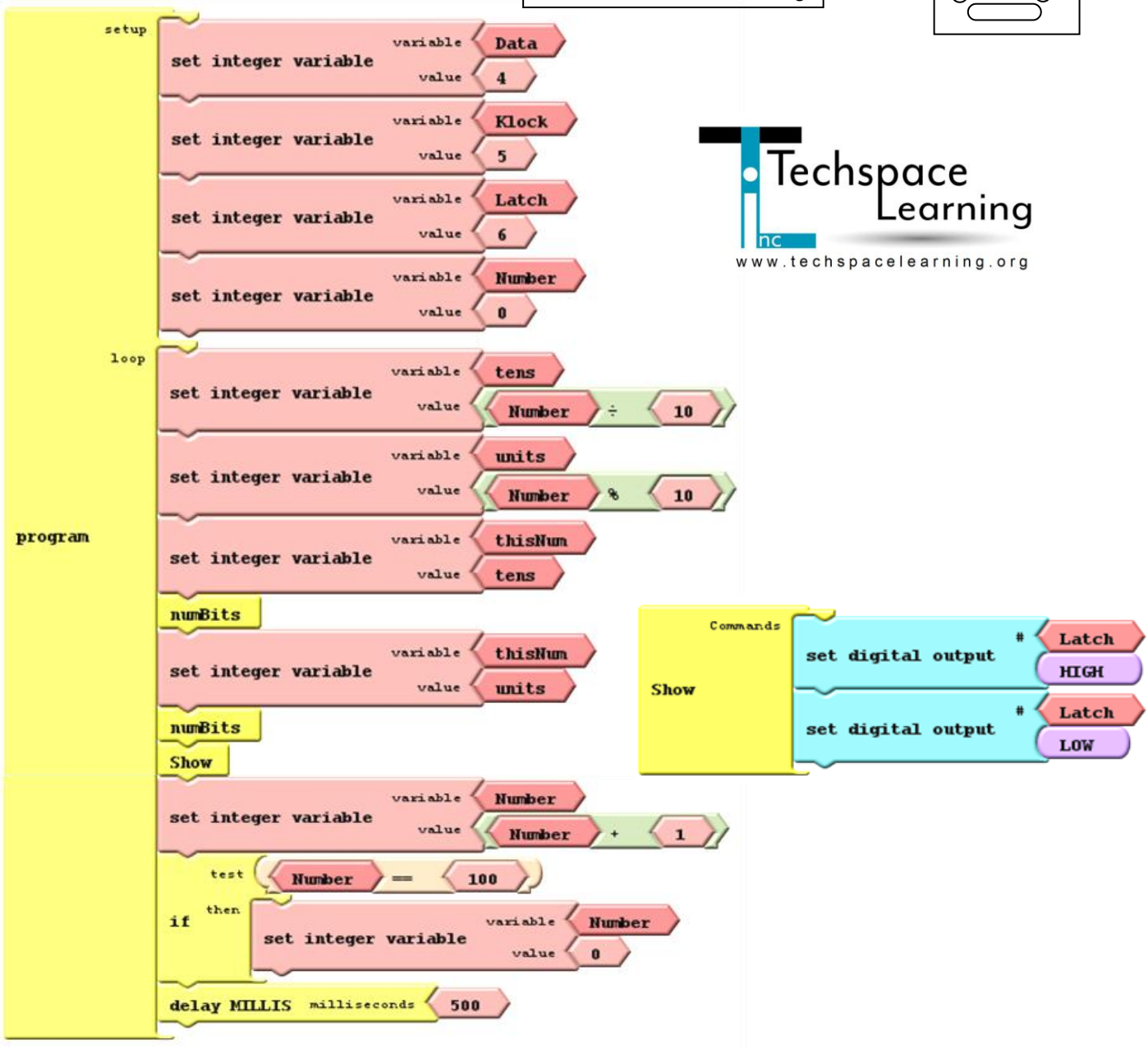
A shift register has a "Data Out", so the last bit gets pushed out the other end and can be "daisy-chained" to another shift register.



The "Clock" and "Latch" pins need to be transferred across as well.



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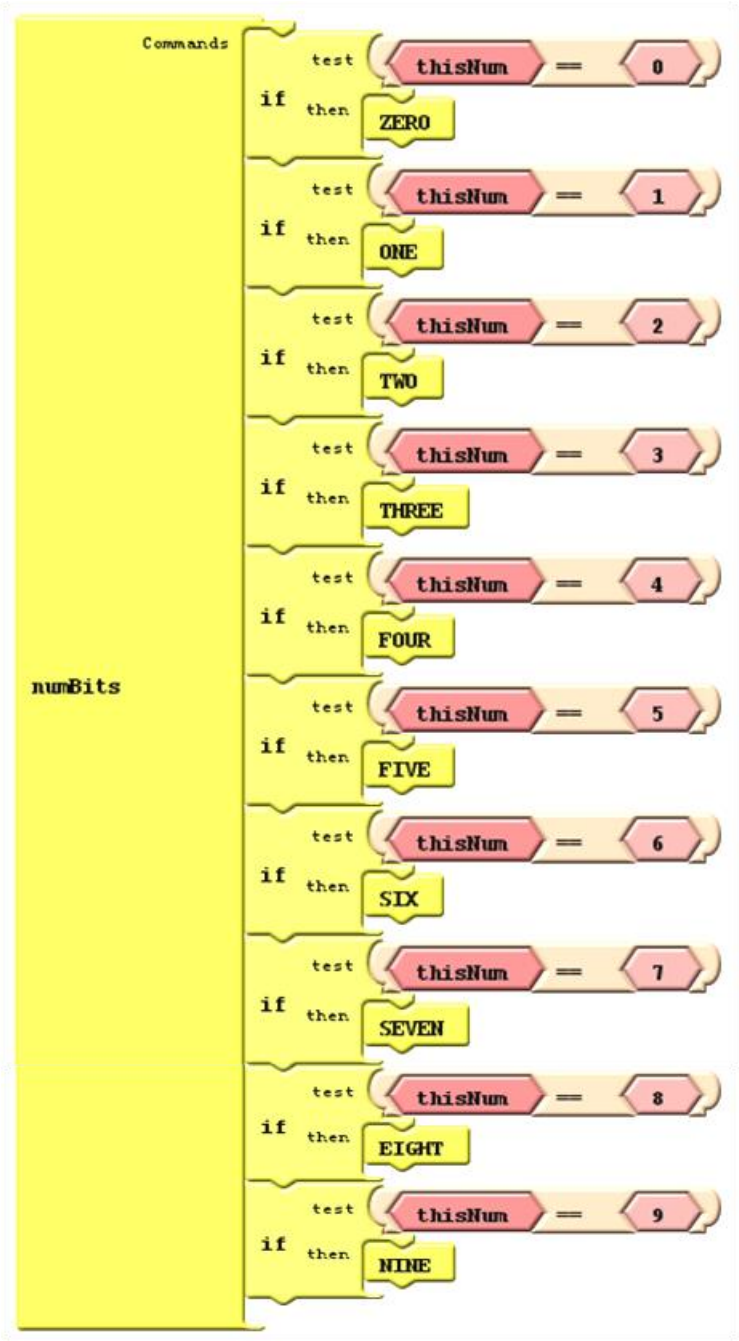

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